

RESUME



VIVEK KUMAR TAMTA
Computer Science & Engineering Department,
G. B Pant Engineering College, Pauri, UK, INDIA.

E-mail: vivek.tamta2010@gmail.com

Mobile: 91-9997806487

CAREER OBJECTIVE:

Want to broaden the horizon of my knowledge and technical skills in the field of Computer science and to pursue a challenging career and be part of a progressive organization that gives scope to enhance my knowledge, skills and to reach the pinnacle in the computing and research field with sheer determination, dedication and hard work. I am interested in Automation and machine learning.

PERSONAL PROFILE:

NAME : Vivek Kumar Tamta
SEX : Male
Parents : Mr. R.P Tamta & Late. Mrs. Sarojini Tamta
Nationality : Indian
Date of birth : 08/08/1986
Marital Status : Married
Linguistic Ability : English, Hindi, Garhwali, Marathi
Passport Number : K4968000
Personal Strength : Leadership skills, Self-confidence, Optimism and Hard Working.
Present Address : G. B Pant Engineering college, Pauri - 246001

WORK EXPERIENCE:

- Worked as an Assistant Professor in the Department of Computer Science & Engineering at G. B Pant Engineering college, Pauri from 22/09/2008 to 19/08/2010 (2 years).
- Worked as a Teaching Assistant in the Department of Computer Science & Engineering at National Institute of Technology, Utrakhnad from 03/07/2012 to 31/08/2012 (2 months).
- Working as an Assistant Professor in the Department of Computer Science & Engineering at G. B Pant Engineering college, Pauri from 04/09/2012 to till date.

ACADEMIC PROFILE:

Examination Passed	University /Board	Year	% Marks Obtained	Class
3 Years Diploma in IT	UBTER, Roorkee	2005	71.76%	1 st Division
B.Tech in Computer Science & Engg	HNBGU, Srinagar	2008	67.28%	1 st Division
M.Tech in Computer Science & Engg	Uttarakhand Technical University, Dehradun	2012	80.13%	1 st Division (Honour)
P.hd in Cloud Computing	Uttarakhand Technical University, Dehradun	Pursuing 16 August 2013

PROJECTS:

1. **M.Tech Project:** “Design of Scalable and Reliable MAC Protocol for safety and Real-Time Communication in V2V VANETs”

In this thesis, I propose a hybrid NEW DS-MAC protocol for channel access in dynamically changing topology and analyze our proposed protocol in the worst condition of the Ad-hoc network terminology under VANETs requirements. Proposed protocol takes care of safety and real time application in V2V VANETs by taking advantage of both contentions based and contention free protocols. Proposed protocol is a self organizing MAC protocol which works specifically for Vehicular communication in the multilane highway scenario and takes advantage of all the localization techniques mounted on the top of the intelligent vehicle like, GPS system. The GPS enabled vehicles compute geographic information from the satellite and communicate it with the other vehicles in the radio range. GPS system also helps in to communicate synchronization information among vehicles in the common radio range. To summarize, proposed design shows the possibility of building a MAC protocol for VANETs that provides high packet delivery rate, supporting the development of Vehicular safety application that demand reliability and scalability. The proposed protocol also provides solution for the real time communication in V2V scenario.

2. **B.Tech Project:** Build a Project on **CENTER AUTOMATION SYSTEM** using Visual Basic at front end and SQL Server 2000 at the back end.
3. **Diploma Project:** Built a Project on **CABLE TV INFORMATION SYSTEM** using VISUAL BASIC at front end and SQL SERVER 2000 at the back end.

WORK DONE:

1. Guided several B.E/B.Tech/MCA Projects.
2. Taken 4 hours regular lectures several times as per needs of the students on holiday
3. Taken classes of Automata Theory, Compiler Design, Principle of Programming Language, Basic Computer Science Engineering and Software Engineering for B.E/B.Tech and MCA at G. B Pant Engineering college and NIT, Uttarakhand INDIA.

EXTRA PORTFOLIOS:

1. Worked as **Visiting Expert faculty advisor** at NIT uttrakhand for 2 months.

TECHNICAL SKILLS:

Programming Language

Back end

Operating System

Simulators

C & C++, JAVA, RSA

SQL

Windows, Linux

Qualnet, Glomosium

SUBJECT TAUGHT:

1. Automata
2. Compiler
3. Data communication & Computer Networks
4. Software Engineering
5. Principle of Programming language
6. Mobile computing
7. Ad-hoc Networks
8. Computer Architecture

PUBLICATIONS

1. Published a paper "**Modified non-recursive algorithm for reconstructing a Binary Tree**", International journal of computer Applications (0975- 8887) volume 43-N.o 10, April 2012.
2. Published a paper "**Dynamic Instruction Scheduling For Microprocessors Having Out Of Order Execution**" Computer Engineering and Intelligent Systems ISSN 2222-1719 (Paper) ISSN 2222-2863 (Online) Vol 3, No.4, 2012.
3. Published a paper "**A Novel Sorting Algorithm and Comparison with Bubble Sort and Insertion Sort**", International journal of computer Applications (0975- 8887) volume 45-No 1, May 2012.
4. Published a paper "**Load balancing Approaches in Grid computing Environment**", International journal of computer Applications (0975- 8887) volume 72-No. 12, June 2013.
5. Published a paper "**Load balancing Approaches for scheduling sequential task in Grid computing environment**", International journal of computer Applications (0975- 8887) volume 78-No 1, September 2013.

EXTRA CO-CURRICULAR ACTIVITIES

1. Attended **three days** workshop on “**Simulation and Modeling Techniques**” conducted by Master of computer Applications department, G. B Pant Engineering college, Pauri. Lectures and hands on experience class taken by JNU, Delhi Faculties held on 2012.
2. Attended **Four days** workshop on “**Rational Software Architect**” conducted by IBM Trainers held on 2013

HOBBIES:

Visiting to new and beautiful places Singing & Drawing

Declaration: I hereby declare that the information furnished above is true to the best of my knowledge and belief.

Vivek Kumar Tamta